



LMT070DICFWD-AKA-1M

LCD Module User Manual

Prepared by: Hehongliang Date: 2017-10-31	Checked by: Date:	Approved by: Date:
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Rev.	Descriptions	Release Date
0.1	Preliminary	2016-12-20
0.2	Assemble Precaution	2017-10-31

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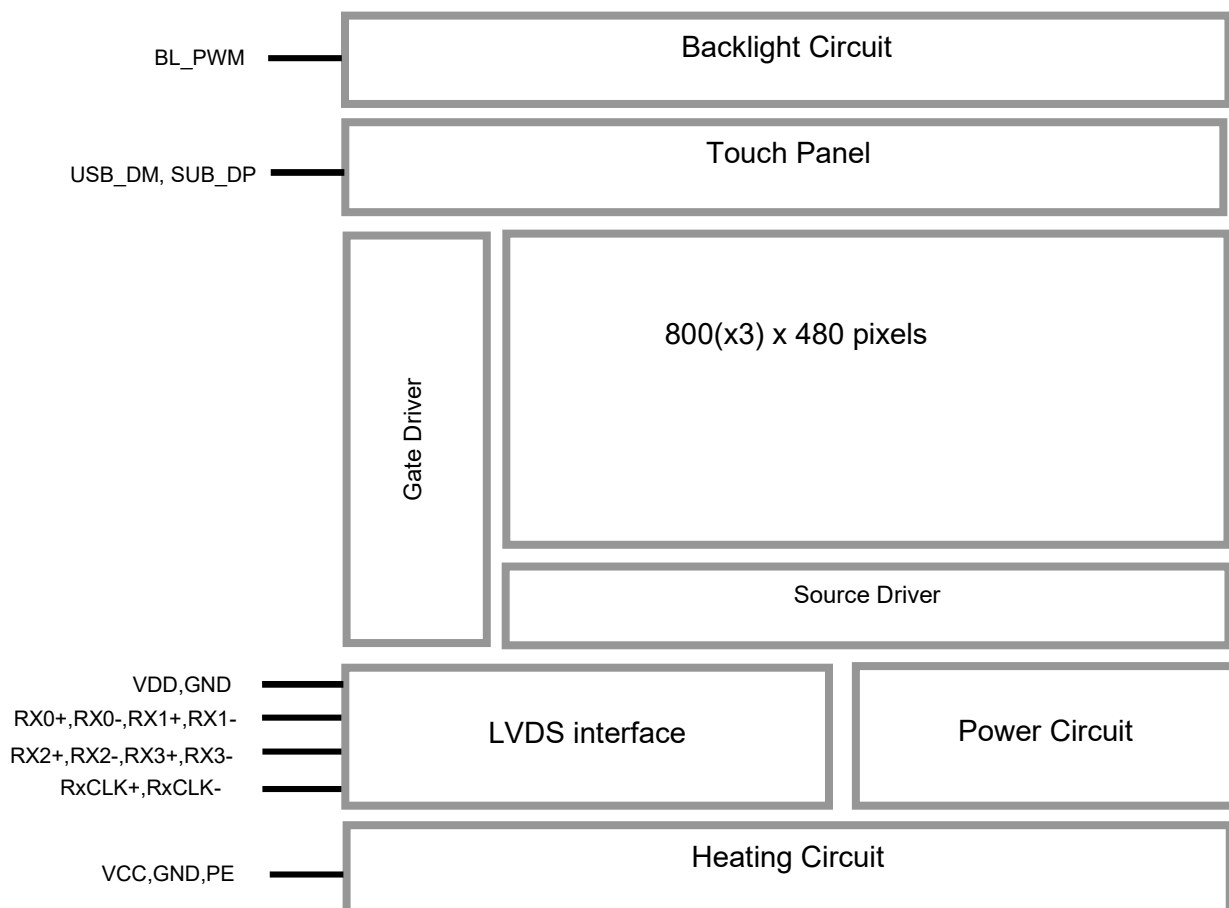
1. General Specification

Signal Interface :	LVDS (JEIDA 24 bits)
Display Mode :	Transmissive with Normally White
Screen Size :	7.0 inch
Outline Dimension :	208.6 x 133.6 x 30.0(mm)(with mounting Bezel) (see outline drawing for details)
Active Area :	154.08x 85.92(mm)
Number of dots :	800x 3 (RGB) x 480
Dot Pitch :	0.1926x 0.179(mm)
Pixel Configuration :	R.G.B. Vertical Stripe
Backlight :	White LED
Viewing Direction :	6 o'clock (Gray scale Inversion) (*1) 12 o'clock (*2)
Operating Temperature :	-35 ~ +70°C
Storage Temperature :	-30 ~ +80°C

Note:

- *1. For saturated color display content (eg. pure-red, pure-green, pure-blue or pure-colors -combinations).
- *2. For “color scales” display content.
- *3. Color tone may slightly change by temperature and driving condition.

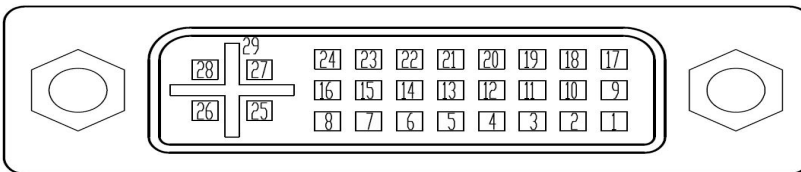
2. Block Diagram



3. Terminal Function

3.1 K1 Terminal(DVI Right angle connector)

Pin No.	Pin Name	IO	Descriptions
1	RX2-	Input	LVDS receiver negative signal channel 2
2	RX2+	Input	LVDS receiver positive signal channel 2
3	GND	Power	Ground
4	BL_PWM	Input	Backlight dimming control(High actives) PWM may be used to adjust the output brightness
5	NC	-	No connection
6	VDD	Power	Positive Power Supply(5.0V)
7	VDD	Power	Positive Power Supply(5.0V)
8	VDD	Power	Positive Power Supply(5.0V)
9	RX1-	Input	LVDS receiver negative signal channel 1
10	RX1+	Input	LVDS receiver positive signal channel 1
11	GND	Power	Ground
12	RX3-	Input	LVDS receiver negative signal channel 3
13	RX3+	Input	LVDS receiver positive signal channel 3
14	VDD	Power	Positive Power Supply(5.0V)
15	GND	Power	Ground
16	GND	Power	Ground
17	RX0-	Input	LVDS receiver negative signal channel 0
18	RX0+	Input	LVDS receiver positive signal channel 0
19	GND	Power	Ground
20	USB_DM	I/O	USB D- signal
21	USB_DP	I/O	USB D+ signal
22	GND	Power	Ground
23	RXCLK+	Input	LVDS receiver positive signal clock
24	RXCLK-	Input	LVDS receiver negative signal clock
25	VDD	Power	Positive Power Supply(5.0V)
26	VDD	Power	Positive Power Supply(5.0V)
27	NC	-	No connection
28	NC	-	No connection
29	GND	Power	Ground
--	SHELL	--	Case Ground



3.2 K3 Terminal

Pin No.	Pin Name	IO	Descriptions
1	VCC	Power	Positive Power Supply(12.0V)
2	GND	Power	Ground
3	PE	--	Case Ground

4. Absolute Maximum Ratings

Items	Symbol	Min.	Max.	Unit	Condition
Power Supply voltage	VDD	-0.3	5.5	V	
Heating Supply voltage	VCC	-0.3	12.5	V	
Operating Temperature	T _{OP}	-35	70	°C	No Condensation
Storage Temperature	T _{ST}	-30	80	°C	No Condensation

Note:

- *1. This rating applies to all parts of the module. And should not be exceeded.
- *2. The operating temperature only guarantees operation of the circuit. The contrast, response speed, and the other specification related to electro-optical display quality is determined at the room temperature, T_{OP}=25°C
- *3. Any Stresses exceeding the Absolute Maximum Ratings may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

5. Electrical Characteristics

5.1 DC Characteristics

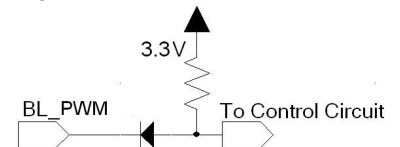
VDD=5.0V, VCC=12.0V, GND=0V, T_a=25°C

Items	Symbol	MIN.	TYP.	MAX.	Unit	Note
Power Supply Voltage	VDD	4.7	5.0	5.3	V	
Heating Supply Voltage	VCC	11.7	12.0	12.3	V	
Power Supply Consumption	I _{dd}	--	500	TBD	mA	*1
Heating Supply Consumption	I _{CC}	--	1.65	TBD	A	
Input High Voltage	V _{IH}	3.0	3.3	-	V	BL_PWM
Input Low Voltage	V _{IL}	GND	-	0.3	V	BL_PWM

Note1:

- *1. BL_PWM=Hi;
- *2. Recommended BL_PWM PWM Freq. is 3kHz.

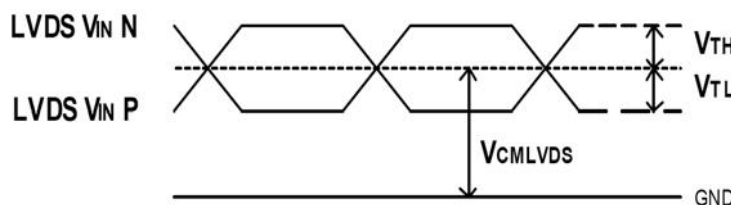
*3. Terminal circuit.



5.2 DC Characteristics(LVDS)

VDD=5.0V, GND=0V, T_a=25°C

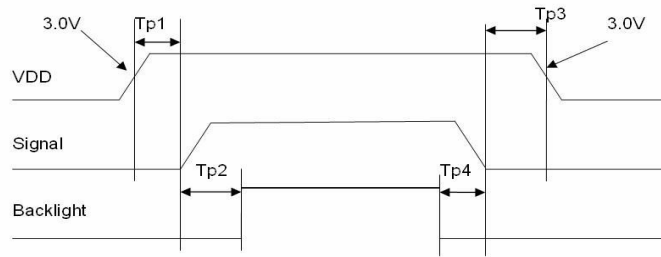
Items	Symbol	MIN.	TYP.	MAX.	Unit	Note
Differential Input High Threshold	V _{TH}	-	-	100	mV	
Differential Input Low Threshold	V _{TL}	-100	-	-	mV	
Input Current	I _{IN}			± 10	uA	
Differential Input common Mode voltage	V _{CMLVDS}	1.65	-	2.1	V	



LVDS DC timing diagram

5.3 POWER ON/OFF SEQUENCE

Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Note
VDD 5.0V to signal starting	Tp1	0	-	50	ms	
Signal starting to backlight on	Tp2	150	-	-	ms	
Signal off to VDD 3.0V	Tp3	0	-	50	ms	
Backlight off to signal off	Tp4	150	-	-	ms	



Interface Power On/Off Sequence

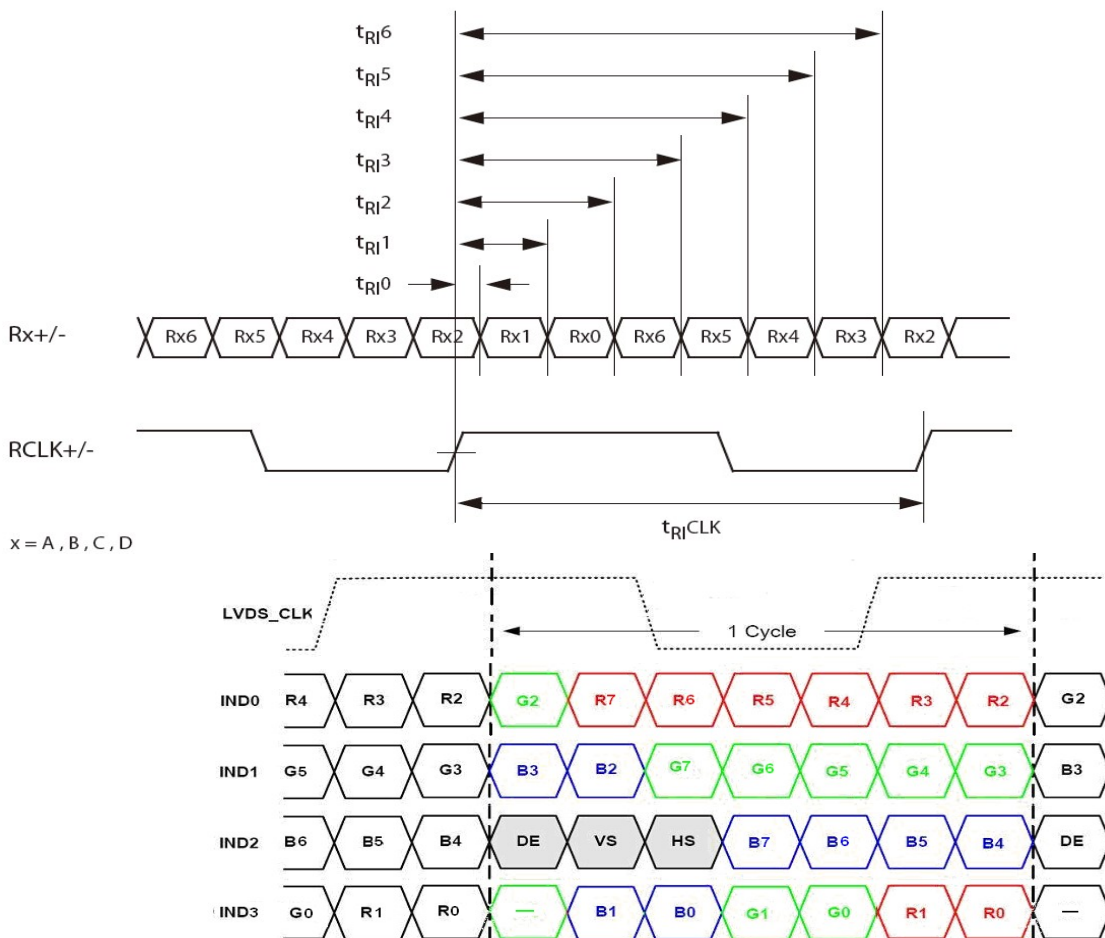
6. AC Characteristics

6.1 AC Characteristics(LVDS)

VDD=5.0V,GND=0V,T_a=25°C

Item	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Input CLK period	t _{RI} CLK	8.9	-	50	ns	
Input Data Position 0 (t _{RI} CLK = 8.9ns)	t _{RI} 0	-0.3	-	+0.3	ns	
Input Data Position 1 (t _{RI} CLK = 8.9ns)	t _{RI} 1	t _{RI} CLK/7-0.3	t _{RI} CLK/7	t _{RI} CLK/7+0.3	ns	
Input Data Position 2 (t _{RI} CLK = 8.9ns)	t _{RI} 2	2t _{RI} CLK/7-0.3	2t _{RI} CLK/7	2t _{RI} CLK/7+0.3	ns	
Input Data Position 3 (t _{RI} CLK = 8.9ns)	t _{RI} 3	3t _{RI} CLK/7-0.3	3t _{RI} CLK/7	3t _{RI} CLK/7+0.3	ns	
Input Data Position 4 (t _{RI} CLK = 8.9ns)	t _{RI} 4	4t _{RI} CLK/7-0.3	4t _{RI} CLK/7	4t _{RI} CLK/7+0.3	ns	
Input Data Position 5 (t _{RI} CLK = 8.9ns)	t _{RI} 5	5t _{RI} CLK/7-0.3	5t _{RI} CLK/7	5t _{RI} CLK/7+0.3	ns	
Input Data Position 6 (t _{RI} CLK = 8.9ns)	t _{RI} 6	6t _{RI} CLK/7-0.3	6t _{RI} CLK/7	6t _{RI} CLK/7+0.3	ns	

Input Clock and Data timing Diagram:



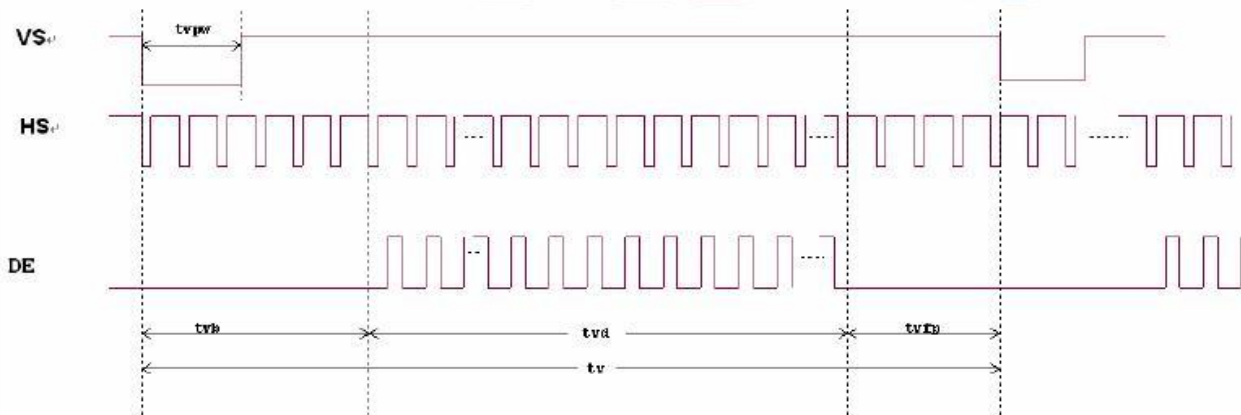
6.2 AC Characteristics(TFT)

Item	Symbol	MIN.	TYP.	MAX.	Unit	Remark
Horizontal Display Area	thd	-	800	-	DCLK	
DCLK Frequency	fclk	26.4	33.3	46.8	MHz	
One Horizontal Line	th	862	1056	1200	DCLK	
HS pulse width	thpw	1	-	40	DCLK	
HS Blanking	thb	46	46	46	DCLK	
HS Front Porch	thfp	16	210	354	DCLK	

Item	Symbol	Values			Unit	Remark
		MIN.	TYP.	MAX.		
Vertical Display Area	tvd	-	480	-	TH	
VS period time	tv	510	525	650	TH	
VS pulse width	tvpw	1	-	20	TH	
VS Blanking	tvb	23	23	23	TH	
VS Front Porch	tvfp	7	22	147	TH	



1 Horizontal input timing diagram.



Vertical input timing diagram.

7. Optical Characteristics

Item	Symbol	Condition	MIN.	TYP.	MAX.	UNIT	Note.
Viewing angle (CR ≥ 10)	θ_L	9 o'clock	60	70	-	degree	*2
	θ_R	3 o'clock	60	70	-		
	θ_T	12 o'clock	40	50	-		
	θ_B	6 o'clock	60	70	-		
Response Time	T_f	Normal $\theta=0^\circ$	-	10	20	msec	*3
	T_r		-	15	30	msec	
Contrast ratio	CR		400	500	-	-	*1
Color chromaticlty	W_x		0.26	0.31	0.26	-	
	W_y		0.28	0.33	0.38	-	
Luminance	L		-	500	-	cd/m ²	*4
Luminance uniformity	Y_U		70	75	-	%	*4

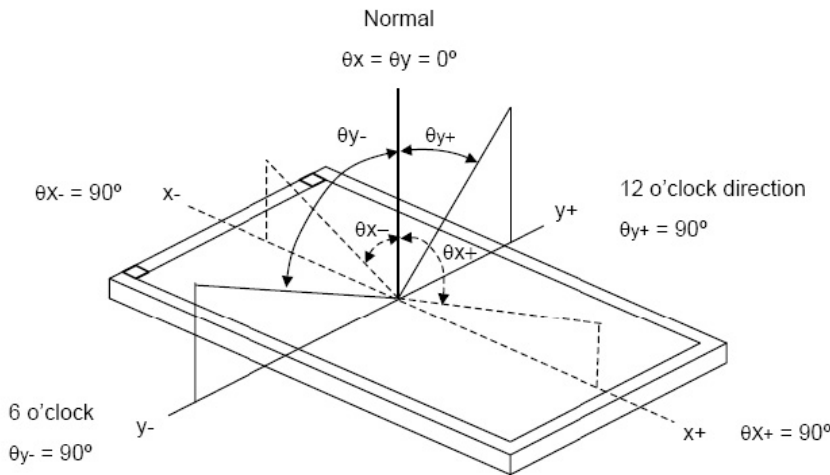
Note:

*1. Definition of Contrast Ratio

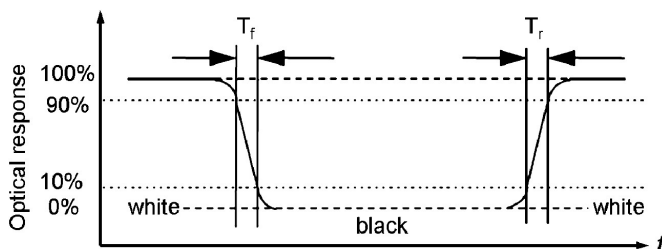
The contrast ratio could be calculate by the following expression:

Contrast Ratio (CR) = Luminanc with all pixels white / Luminance with all pixels black

*2 Definition of Viewing Angle



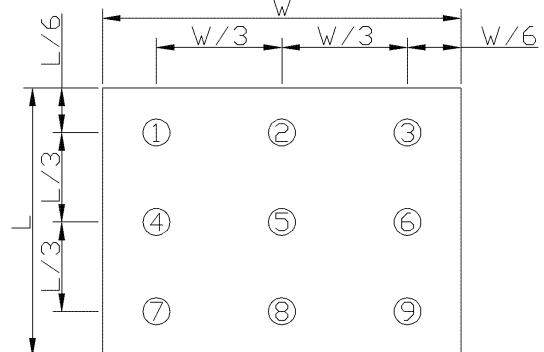
*3 Definition of response time



*4 Definition of Luminance Uniformity

Luminance uniformity (Lu)=

Min. Luminance form pt1~pt9 / Max Luminance form Pt1~pt9



8. Precautions for Use of LCD Modules

Please refer to "LCD-Module-Design-Handling-Precaution.pdf".

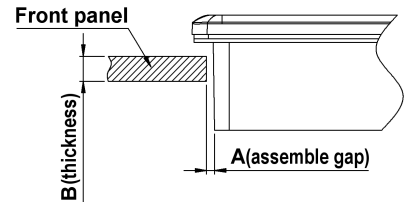
9. Assemble Precaution

安装注意事项

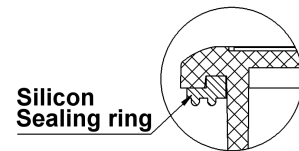
- Customer front panel opening and thickness for TOPWAY display module should be fit for its assembling and sealing.
 - The suggested assemble gap(A) should be about 0.3~0.5mm on each side.
 - The suggested front panel thickness(B) should be about 1.5~4.0mm.

客户面板开窗及厚度应适合 TOPWAY 显示模块的安装及密封。

- 建议每边安装间隙(A)约为 0.3 ~0.5mm.
- 建议面板厚度(B) 约为 1.5~4.0mm.



- A silicon sealing ring ships with TOPWAY display module. It should be in place before assembling to the front panel. TOPWAY 显示模块上的硅胶密封圈在安装时确保嵌入到位。



- It should fix the TOPWAY display module into the front panel with two steps.
 - Pre-fixing: Slightly tighten the screws on beam clamp in sequence as picture on the right side.
 - Final-fixing: Tighten the fixing screws on beam clamp in sequence as well with twist torque about 8~10kg.cm (*1) . and put the beam clamp straight.

Note:

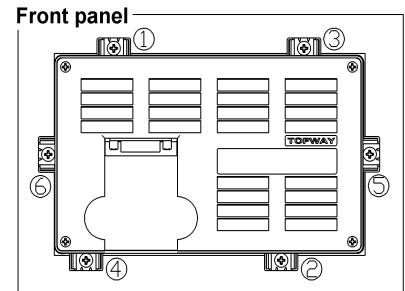
*1. Over tightening might damage the shell and cause bad sealing result.

应分两步将 TOPWAY 显示模块固定在面板上。

- 预紧: 将卡扣螺钉按右图所示顺序稍加预紧。
- 紧定:再次按顺序用 **8~10kg.cm** 扭力拧紧卡扣螺钉(*1),并注意卡扣置正无歪斜。

注:

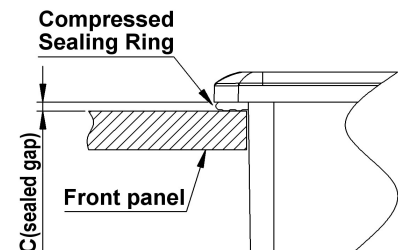
*1. 过度拧紧可能会损坏外壳和影响密封效果。



- It is strongly suggested to check the seal balancing of the four-side of the TOPWAY display module.
 - The suggested after assemble sealed gap(C) should be about 1.0~1.5mm.

需注意检查 TOPWAY 显示模块四周在安装后保证平衡密封。

- 建议组装后的密封间隙(C)约为 1.0 ~1.5 mm.



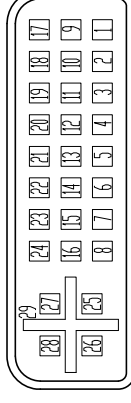
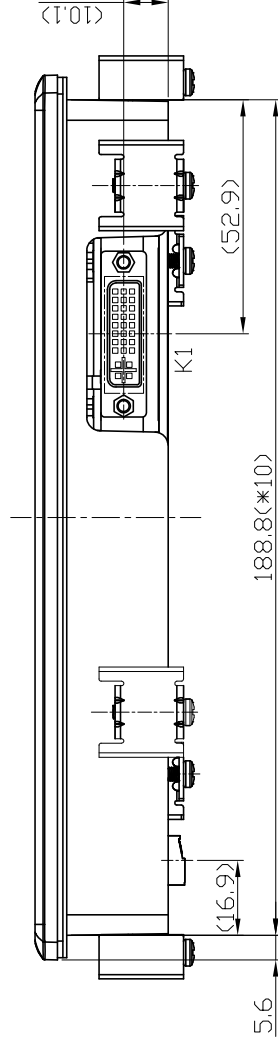
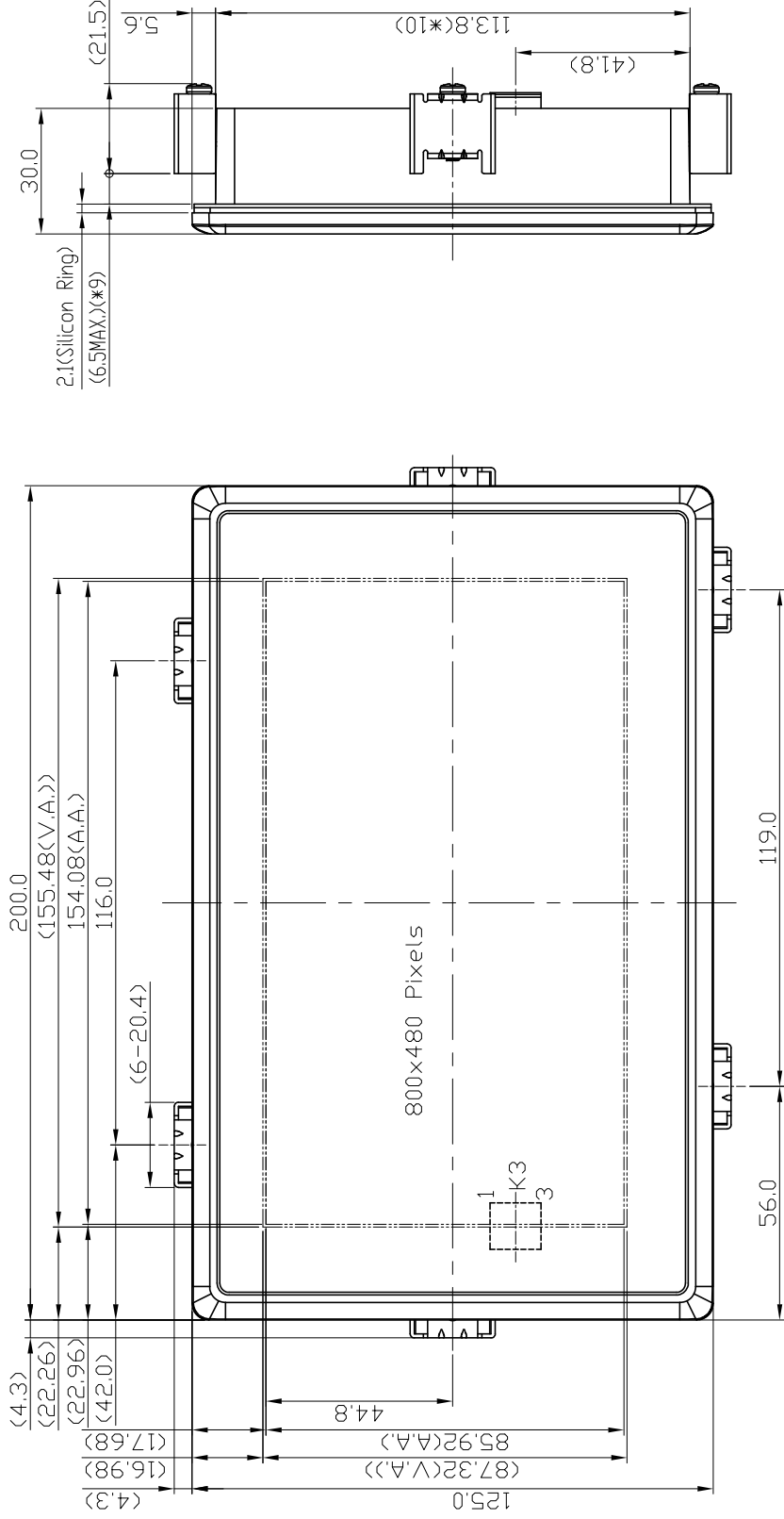
- Others:
 - Never hot plug the device! Power off the device before connect or disconnect the display module.
 - Don't forget to remove the cover protective film for normal operation.

其它:

- 视频线禁止带电插拔! 在连接或断开显示模块之前先关闭设备电源。
- 使用前请揭去保护膜。

K3 Terminal	
No	Pin Name
1	VCC(12V)
2	GND
3	PE

K1 Terminal	
No	Pin Name
1	RX2-
2	RX2+
3	GND
4	BL_PWM
5	NC
6	VDD
7	VDD
8	VDD
9	RX1-
10	RX1+
11	GND
12	RX3-
13	RX3+
14	VDD
15	GND
16	GND
17	RX0-
18	RX0+
19	GND
20	USB_DM
21	USB_DP
22	GND
23	RXCLK+
24	RXCLK-
25	VDD
26	VDD
27	NC
28	NC
29	GND



SHELL(CASE GND)
K1 Details
Scale=3/1

Note:

- *1. LCD Display Type: TFT, Transmissive
- *2. Pixel Arrangement: RGB-STRIPE
- *3. Operating Voltage : 5.0V(VDD), 12.0V(VCC)
- *4. Backlight : White LED
- *5. Touch Panel Type: Resistive Touch Panel
- *6. K1: DVI Connector (F)
- *7. K3: 3Pin Phoenix MC 15/3-ST-3.81 Or Equivalent
- *8. Signal Interface : LVDS(JEIDA 24bits)
- *9. Operating Temperature : -35°C~70°C
- *10. Applicable assemble panel thickness = 6.5MAX.
- *11. Applicable assemble panel Opening(Min)

C	
B	
A	
Rev/Note	Date
Dwg Title	

Dwg No.	MK-005721-1-1	Date	2016-09-26
Scale	4/5	Unit	mm
Approved		Checked	
Drawn	He Hong Liang		
Paper Size	A3		

TOPWAY