



深圳市拓普微科技开发有限公司

SHENZHEN TOPWAY TECHNOLOGY CO., LTD.

# LMT018DNBFWD-2

## LCD Module User Manual

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Rev.	Descriptions	Release Date
0.1	Preliminary New release	2020-06-09
0.2	Update Absolute Maximum Ratings	2021-02-04

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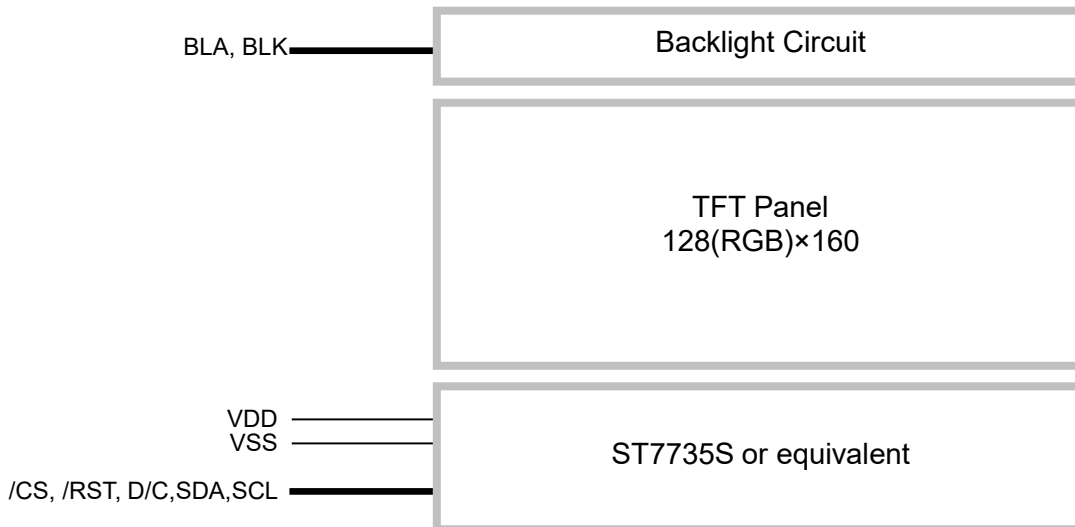
### 1. Basic Specifications

Screen Size(Diagonal) :	1.8"
Color Depth:	65k / 262k color
Number of dots :	128(RGB) x 160
Active Area :	28.03x35.04 mm
Dot Pitch :	0.219x0.219 mm
Display Technology :	a-Si TFT active matrix
Display Mode :	Transmissive / Positive
Pixel Configuration :	RGB Stripe
Viewing Direction :	12H (*1) (gray scale inverse) 6H (*2)
Polarizer Surface Treatment:	Glare
Backlight Type:	LEDs
Outline Dimension :	34.0 x 43.78 x 2.6 mm (exclude FPC , see dwg for details)
Operating Temperature :	-20 ~ +70°C (No Condensation)
Storage Temperature :	-30 ~ +80°C (No Condensation)

Note:

- \*1. For saturated color display content (eg. pure-red, pure-green, pure-blue or pure-colors-combinations).
- \*2. For "color scales" display content.
- \*3. Color tone may slightly change by temperature and driving condition.

### Block Diagram



**Terminal Functions**

<b>Pin No.</b>	<b>PIN Name</b>	<b>I/O</b>	<b>Descriptions</b>
1	VSS	Power	Negative power supply,0V
2	VDD	Power	Positive power supply
3	SCL	Input	Serial interface clock pin
4	/RST	Input	Reset signal /RESET = L, Initialization is executed /RESET = H, Normal running.
5	/CS	Input	Chip Select /CS=L, enable access to the LCD module /CS=H, disable access to the LCD module
6	SDA	I/O	Serial input/output signal pin
7	D/C	Input	Register Select D/C = H, Transferring the Display Data D/C = L, Transferring the Control Data
8	BLK	Power	Negative power for LED backlight
9	BLA	Power	Positive power for LED backlight

## 2. Absolute Maximum Ratings

Items	Symbol	Min.	Max.	Unit	Condition
Supply Voltage	$V_{DD}$	-0.3	+4.6	V	$V_{SS} = 0V$
Input Voltage	$V_{IN}$	-0.3	+4.6	V	$V_{SS} = 0V$
Operating Temperature	$T_{OP}$	-20	+70	°C	No Condensation
Storage Temperature	$T_{ST}$	-30	+80	°C	No Condensation
High Temperature & High Humidity Storage	-	-	+60°C & 90% RH	-	No Condensation IEC60068-2-78 GB/T2423.3

Cautions:

Any Stresses exceeding the Absolute Maximum Ratings may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

## 3. Electrical Characteristics

### DC Characteristics

$V_{SS}=0V, T_{OP}=25^{\circ}C$

Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition / Application Pin
Operating Voltage	$V_{DD}$	2.6	3.3	3.6	V	VDD
Input High Voltage	$V_{IH}$	$0.8 \times V_{DD}$	-	$V_{DD}$	V	/RST, /CS, D/C,
Input Low Voltage	$V_{IL}$	$V_{SS}$	-	$0.2 \times V_{DD}$	V	SDA, SCL
Operating Current	$I_{DD}$	-	1.8	4.5	mA	VDD (*1)

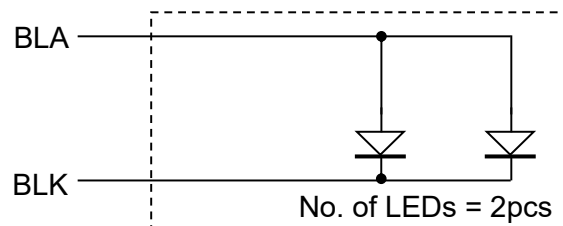
### LED Backlight Circuit Characteristics

$BLK=0V, I_{BLA}=30mA, T_{OP} = 25^{\circ}C$

Items	Symbol	MIN.	TYP.	MAX.	Unit	Applicable Pin
Forward Voltage	$V_{f_{BLA}}$	-	3.2	-	V	BLA
Forward Current	$I_{BLA}$	-	30	-	mA	BLA
LED lifetime	L	-	20000	-	Hours	

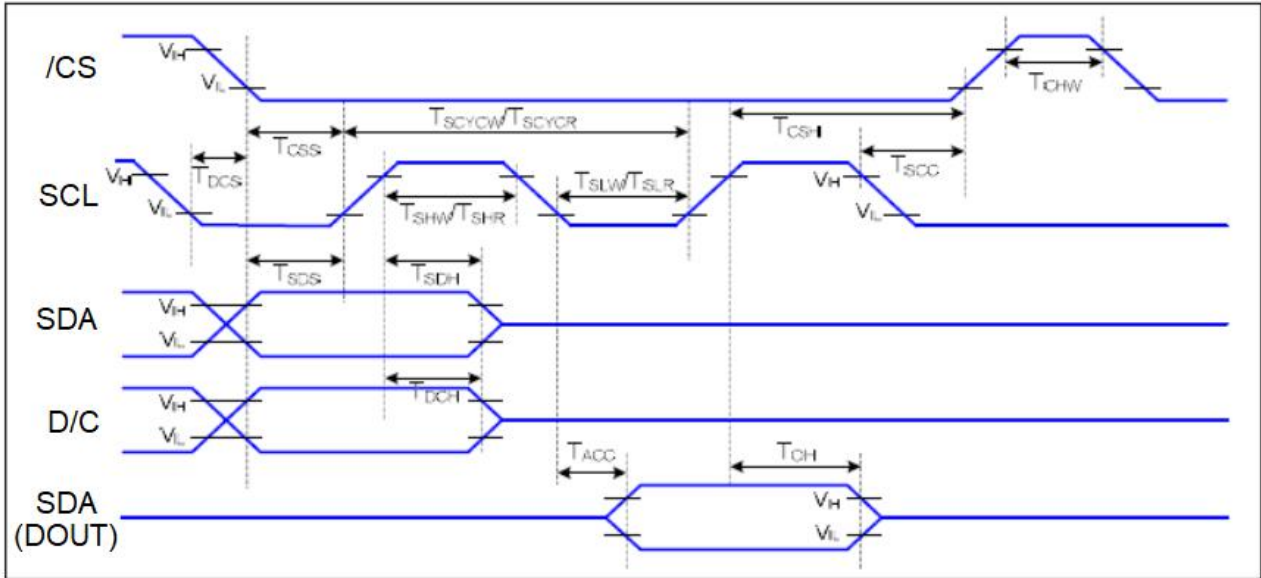
Cautions:

Exceeding the recommended driving current could cause substantial damage to the backlight and shorten its lifetime.



**AC Characteristics**

**4-SPI Mode Timing**



V<sub>SS</sub>=0V, V<sub>DD</sub>=2.8V, T<sub>OP</sub>=25°C

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
/CS	TCSS	Chip Select Setup Time (Write)	45		ns	
	TCSH	Chip Select Hold Time (Write)	45		ns	
	TCSS	Chip Select Setup Time (Read)	60		ns	
	TSCC	Chip Select Hold Time (Read)	65		ns	
	TCHW	Chip Select "H" Pulse Width	40		ns	
SCL	TSCYCW	Serial Clock Cycle (Write)	66		ns	-Write Command & Data Ram
	TSHW	SCL "H" Pulse Width (Write)	15		ns	
	TSLW	SCL "L" Pulse Width (Write)	15		ns	
	TSCYCR	Serial Clock Cycle (Read)	150		ns	-Read Command & Data Ram
	TSHR	SCL "H" Pulse Width (Read)	60		ns	
	TSLR	SCL "L" Pulse Width (Read)	60		ns	
D/C	TDCS	D/C Setup Time	10		ns	
	TDCH	D/C Hold Time	10		ns	
SDA (DIN) (DOUT)	TSDS	Data Setup Time	10		ns	For Maximum CL=30pF For Minimum CL=8pF
	TSDH	Data Hold Time	10		ns	
	TACC	Access Time	10	50	ns	
	TOH	Output Disable Time	15	50	ns	

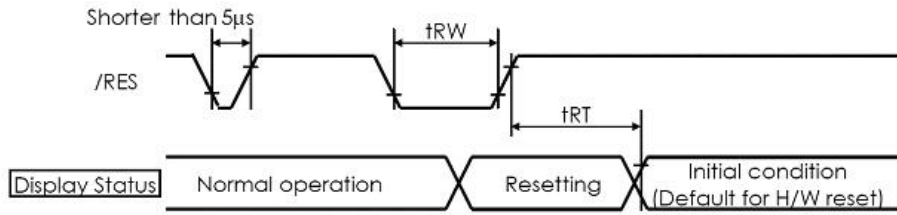
Note:

\*1. Input signal rise/fall time should be less than 15ns .

\*2. All timing is using 20% and 80% of VDD as the reference.

\*3. Please refer to ST7735S datasheet for details

**Reset Timing**



Item	Symbol	MIN.	TYP.	MAX.	Unit
Reset LOW pulse width	$t_{RW}$	10	-	-	us
Reset time	$T_{RT}$	-	-	120	ms

**4. Optical Characteristics**

Item	Symbol	Min.	Typ.	Max.	Unit	Note/ Condition
View Angles	$\theta T$	50	60	-	Degree	CR≥10 (*1, *3)
	$\theta B$	40	50	-		
	$\theta L$	50	60	-		
	$\theta R$	50	60	-		
Contrast Ratio	CR	300	500	-		$\theta=0^\circ$ (*4)
Response Time	$T_{ON}$	-	20	30	ms	25°C (*5)
	$T_{OFF}$					
Chromaticity	white	x	0.226	0.276	0.326	Backlight is on (*6)
		y	0.252	0.302	0.352	
	red	x	-	TBD	-	
		y	-	TBD	-	
	green	x	-	TBD	-	
		y	-	TBD	-	
blue	x	-	TBD	-		
	y	-	TBD	-		
Uniformity	U	80	-	-	%	$\theta=0^\circ, \Phi=0^\circ$ (*1,*2)
NTSC		-	50	-	%	$\theta=0^\circ, \Phi=0^\circ$ (*1, *6)
Luminance	L	150	180	-	cd/m <sup>2</sup>	$\theta=0^\circ, \Phi=0^\circ$ (*1,*2)

Test Conditions:

1. If= 30mA, and the ambient temperature is 25°C.

Note:

\*1. Measurement

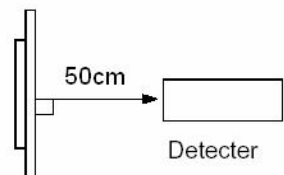
The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots.

Measurement equipment PR-705 (Φ8mm)

Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature:  $T_a=25^\circ C$ .
- Adjust operating voltage to get optimum contrast at the center of the display.

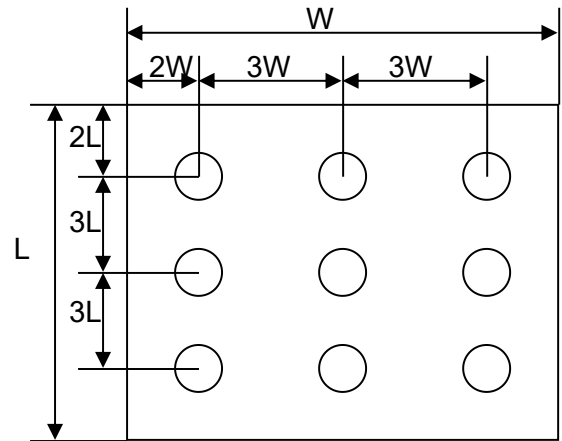
Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.



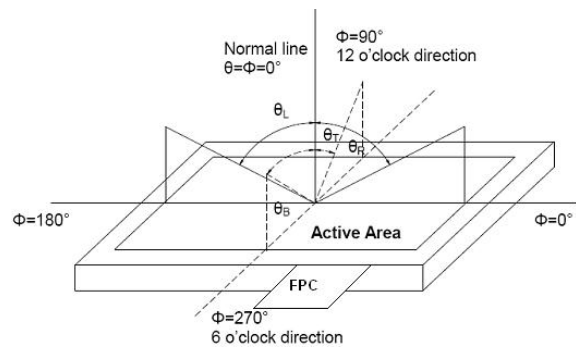
\*2. The luminance uniformity  
It is calculated by using following formula.

$$\Delta Bp = Bp (\text{Min.}) / Bp (\text{Max.}) \times 100 (\%)$$

Bp (Max.) = Maximum brightness in 9 measured spots  
Bp (Min.) = Minimum brightness in 9 measured spots.  
Measurement equipment PR-705 (Φ8mm)



\*3. The definition of viewing angle:  
Refer to the graph below marked by  $\theta$  and  $\phi$

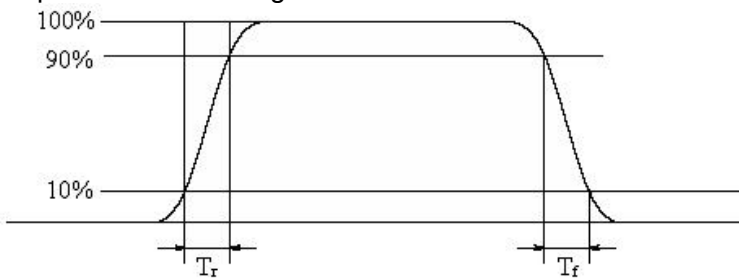


\*4. The definition of contrast ratio (Test LCM using PR-705):

$$\text{Contrast Ratio (CR)} = \frac{\text{Luminance When LCD is at "White" state}}{\text{Luminance When LCD is at "Black" state}}$$

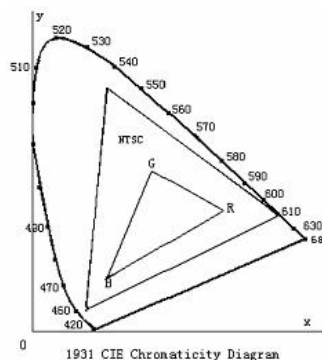
(Contrast Ratio is measured in optimum common electrode voltage)

\*5. Definition of Response time. (Test LCD using DMS501):  
The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



\*6: Definition of Color of CIE Coordinate and NTSC Ratio.

$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$





5. Functions

Instruction	Refer	D/C	X	WRX	RDX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
NOP	0	0	0	↑	1	-	0	0	0	0	0	0	0	0	(00h)	No Operation
SWRESET	0	0	0	↑	1	-	0	0	0	0	0	0	0	1	(01h)	Software Reset
		0	↑	1	1	-	0	0	0	0	0	1	0	0	(04h)	Read Display ID
		1	1	↑	↑	-	-	-	-	-	-	-	-	-		Dummy Read
		1	1	↑	↑	-	-	-	-	-	-	-	-	-		Dummy Read
		1	1	↑	↑	-	-	-	-	-	-	-	-	-		Dummy Read
RDDID	0	1	1	↑	↑	-	ID17	ID16	ID15	ID14	ID13	ID12	ID11	ID10		ID1 Read
		1	1	↑	↑	-	1	ID26	ID25	ID24	ID23	ID22	ID21	ID20		ID2 Read
		1	1	↑	↑	-	ID37	ID36	ID35	ID34	ID33	ID32	ID31	ID30		ID3 Read
		0	↑	1	1	-	0	0	0	0	1	0	0	1	(09h)	Read Display Status
		1	1	↑	↑	-	-	-	-	-	-	-	-	-		Dummy Read
		1	1	↑	↑	-	-	-	-	-	-	-	-	-		Dummy Read
RDDST	0	1	1	↑	↑	-	BSTON	MY	MX	MV	ML	RGB	MH	ST24		
		1	1	↑	↑	-	ST23	IPEF2	IPEF1	IPEF0	IDMON	PTLON	SLOUT	NORON		
		1	1	↑	↑	-	VSSON	ST14	INVON	ST12	ST11	DISON	TEON	GCS2		
		1	1	↑	↑	-	GCS1	GCS0	TEM	ST4	ST3	ST2	ST1	ST0		
		0	↑	1	1	-	0	0	0	0	1	0	1	0	(0Ah)	Read Display Power Mode
		1	1	↑	↑	-	-	-	-	-	-	-	-	-		Dummy Read
RDDPM	0	1	1	↑	↑	-	BSTON	IDMON	PTLON	SLOUT	NORON	DISON	-	-		
		0	↑	1	1	-	0	0	0	0	1	0	1	1	(0Bh)	Read Display MADCTL
		1	1	↑	↑	-	-	-	-	-	-	-	-	-		Dummy Read
		1	1	↑	↑	-	MY	MX	MV	ML	RGB	MH	-	-		
RDD	0	0	↑	1	1	-	0	0	0	0	1	1	0	0	(0Ch)	Read Display Pixel Format
		1	1	↑	↑	-	-	-	-	-	-	-	-	-		Dummy Read
COLMOD	0	1	1	↑	↑	-	0	0	0	0	-	IPEF2	IPEF1	IPEF0		
		1	1	↑	↑	-	0	0	0	0	1	1	0	1	(0Dh)	Read Display Image Mode
		1	1	↑	↑	-	-	-	-	-	-	-	-	-		Dummy Read
RDDIM	0	1	1	↑	↑	-	-	-	-	-	-	-	-	-		Dummy Read
		1	1	↑	↑	-	VSSON	D6	INVON	-	-	GCS2	GCS1	GCS0		
		0	↑	1	1	-	0	0	0	0	1	1	1	0	(0Eh)	Read Display Signal Mode
		1	1	↑	↑	-	-	-	-	-	-	-	-	-		Dummy Read
RDDSM	0	1	1	↑	↑	-	-	-	-	-	-	-	-	-		Dummy Read
		1	1	↑	↑	-	TEON	TEM	-	-	-	-	-	-		
		0	↑	1	1	-	0	0	0	0	1	1	1	1	(0Fh)	Read Display Self-diagnostic result
		1	1	↑	↑	-	-	-	-	-	-	-	-	-		Dummy Read
RDDSDR	0	1	1	↑	↑	-	RELD	FUND	ATTD	BRD	-	-	-	-		

Display Commands

Instruction	Refer	D/CX	WFX	RDX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
SLEEPIN	0	0	↑	1	-	0	0	0	1	0	0	0	0	(10h)	Sleep In & Booster Off
SLEPOUT	0	0	↑	1	-	0	0	0	1	0	0	0	1	(11h)	Sleep Out & Booster On
PTLON	0	0	↑	1	-	0	0	0	1	0	0	1	0	(12h)	Partial Mode On
NORON	0	0	↑	1	-	0	0	0	1	0	0	1	1	(13h)	Partial Off (Normal)
INVON	0	0	↑	1	-	0	0	1	0	0	0	0	0	(20h)	Display Inversion Off (Normal)
INVOFF	0	0	↑	1	-	0	0	1	0	0	0	0	1	(21h)	Display Inversion On
GAMSET	0	0	↑	1	-	0	0	1	0	0	1	1	0	(26h)	Gamma Curve Select
DISPOFF	0	0	↑	1	-	0	0	1	0	1	0	0	0	(28h)	Display Off
DISPON	0	0	↑	1	-	0	0	1	0	1	0	0	1	(29h)	Display On
		0	↑	1	-	0	0	1	0	1	0	1	0	(2ah)	Column Address Set
CASET	0	1	↑	1	-	XS15	XS14	XS13	XS12	XS11	XS10	XS9	XS8		X Address Start: 0≤XS≤X
		1	↑	1	-	XS7	XS6	XS5	XS4	XS3	XS2	XS1	XS0		X Address End: S≤XE≤X
		1	↑	1	-	XE15	XE14	XE13	XE12	XE11	XE10	XE9	XE8		X
		1	↑	1	-	XE7	XE6	XE5	XE4	XE3	XE2	XE1	XE0		RowAddress Set
		0	↑	1	-	0	0	1	0	1	0	1	1	(2bh)	
RASET	0	1	↑	1	-	YS15	YS14	YS13	YS12	YS11	YS10	YS9	YS8		Y Address Start: 0≤YS≤Y
		1	↑	1	-	YS7	YS6	YS5	YS4	YS3	YS2	YS1	YS0		
		1	↑	1	-	YE15	YE14	YE13	YE12	YE11	YE10	YE9	YE8		Y Address End: S≤YE≤Y
		1	↑	1	-	YE7	YE6	YE5	YE4	YE3	YE2	YE1	YE0		
RAMWTR	0	0	↑	1	-	0	0	1	0	1	1	0	0	(2Ch)	Memory Write
		1	↑	1	-	D7	D6	D5	D4	D3	D2	D1	D0		Write Data
		0	↑	1	-	0	0	1	0	1	1	0	1	(2Dh)	LUT for 4k,65k,262k Color display
		1	↑	1	-			R005	R004	R003	R002	R001	R000		Red Tone 0
		1	↑	1	-										
		1	↑	1	-										
		1	↑	1	-			Ra5	Ra4	Ra3	Ra2	Ra1	Ra0		Red Tone "a"
		1	↑	1	-										
		1	↑	1	-			G005	G004	G003	G002	G001	G000		Green Tone 0
		1	↑	1	-										
		1	↑	1	-										
		1	↑	1	-			Gb5	Gb4	Gb3	Gb2	Gb1	Gb0		Green Tone "b"
		1	↑	1	-										
		1	↑	1	-			B005	B004	B003	B002	B001	B000		Blue Tone 0
		1	↑	1	-										
		1	↑	1	-										
		1	↑	1	-			Bc5	Bc4	Bc3	Bc2	Bc1	Bc0		Blue Tone "c"
		0	↑	1	-	0	0	1	0	1	1	1	0	(2Eh)	Memory Read
		1	↑	1	-										
		1	↑	1	-										
RAMRD	0	1	↑	1	-										Dummy Read
		1	↑	1	-	D7	D6	D5	D4	D3	D2	D1	D0		Read Data

Instruction	Refer	D/CX	WRX	RDX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
PTLAR	10.1.25	0	↑	1	-	0	0	1	1	0	0	0	0	(30h)	Partial Start/End Address Set
		1	↑	1	-	PSL7	PSL6	PSL5	PSL4	PSL3	PSL2	PSL1	PSL0		Partial Start Address (0.1.2...P)
		1	↑	1	-	PEL15	PEL14	PEL13	PEL12	PEL11	PEL10	PEL9	PEL8		Partial End Address (0.1.2...P)
		1	↑	1	-	PEL7	PEL6	PEL5	PEL4	PEL3	PEL2	PEL1	PEL0		Scroll area set
SCRLAR	10.1.26	0	↑	1	-	0	0	1	1	0	0	1	1	(33h)	Scroll area set
		1	↑	1	-	TFA7	TFA6	TFA5	TFA4	TFA3	TFA2	TFA1	TFA0		Top fixed area (0.1, 2, ... 161)
		1	↑	1	-	VSAT	VSA6	VSA5	VSA4	VSA3	VSA2	VSA1	VSA0		Vertical scroll area (0.1, 2, ... 161)
		1	↑	1	-	BFA7	BFA6	BFA5	BFA4	BFA3	BFA2	BFA1	BFA0		Bottom fixed area (0.1, 2, ... 161)
TEOFF	10.1.27	0	↑	1	-	0	0	1	1	0	1	0	0	(34h)	Tearing effect line off
		0	↑	1	-	0	0	1	1	0	1	0	1	(35h)	Tearing Effect Mode Set & on
TEON	10.1.28	0	↑	1	-	0	0	1	1	0	1	0	1	(36h)	Memory Data Access Control
		1	↑	1	-	-	-	-	-	-	-	-	TEM		Mode1: TEM="0" Mode2: TEM="1"
MADCTL	10.1.29	0	↑	1	-	0	0	1	1	0	1	1	0	(36h)	Memory Data Access Control
		1	↑	1	-	MY	MX	MV	ML	RGB	MH	-	-		Scroll Start Address of RAM
VSCSAD	10.1.30	0	↑	1	-	0	0	1	1	0	1	1	1	(37h)	Scroll Start Address of RAM
		1	↑	1	-	SSA7	SSA6	SSA5	SSA4	SSA3	SSA2	SSA1	SSA0		SSA=0,1,2,...,161
IDMOFF	10.1.31	0	↑	1	-	0	0	1	1	1	0	0	0	(38h)	Idle Mode Off
		0	↑	1	-	0	0	1	1	1	0	0	1	(39h)	Idle Mode On
COLMOD	10.1.33	0	↑	1	-	0	0	1	1	1	0	1	0	(34h)	Interface Pixel Format
		1	↑	1	-	-	-	-	-	-	IFPF2	IFPF1	IFPF0		Interface Format
RDID1	10.1.34	0	↑	1	-	1	1	0	1	1	0	1	0	(DAh)	Read ID1
		1	↑	1	-	-	-	-	-	-	-	-	-		Dummy Read
RDID2	10.1.35	0	↑	1	-	ID17	ID16	ID15	ID14	ID13	ID12	ID11	ID10		Read Parameter
		1	↑	1	-	-	-	-	-	-	-	-	-		Dummy Read
RDID3	10.1.36	0	↑	1	-	ID26	ID25	ID24	ID23	ID22	ID21	ID20			Read Parameter
		1	↑	1	-	1	0	1	1	1	1	0	0		(DCh) Read ID3
RDID3	10.1.36	1	↑	1	-	-	-	-	-	-	-	-	-		Dummy Read
		1	↑	1	-	ID37	ID36	ID35	ID34	ID33	ID32	ID31	ID30		Read Parameter

Notes:

1. There will be no abnormal visible effects on the display when S/W or H/W Reset are applied.
2. After Powered-On Reset finishes within 10% after both VDD & VDDI are applied.
3. Mode 1 means Tearing Effect Output Line consists of V-Blanking Information only.
4. Please refer to ST7735S datasheet for details

**Power off the LCD Module**

It recommends that enter Sleep Mode before power off the LCD module.

**Refreshing The LCD Module**

It recommends that the operating modes and display contents be refreshed periodically to prevent the effect of unexpected noise.

## 6. LCD Module Design and Handling Precautions

- Please ensure V0, VCOM is adjustable, to enable LCD module get the best contrast ratio under different temperatures, view angles and positions.
- Normally display quality should be judged under the best contrast ratio within viewable area. Unexpected display pattern may come out under abnormal contrast ratio.
- Never operate the LCD module exceed the absolute maximum ratings.
- Never apply signal to the LCD module without power supply.
- Keep signal line as short as possible to reduce external noise interference.
- IC chip (e.g. TAB or COG) is sensitive to light. Strong light might cause malfunction. Light sealing structure casing is recommended.
- Make sure there is enough space (with cushion) between case and LCD panel, to prevent external force passed on to the panel; otherwise that may cause damage to the LCD and degrade its display result.
- Avoid showing a display pattern on screen for a long time (continuous ON segment).
- LCD module reliability may be reduced by temperature shock.
- When storing and operating LCD module, avoids exposure to direct sunlight, high humidity, high or low temperature. They may damage or degrade the LCD module.
- Never leave LCD module in extreme condition (max./min storage/operate temperature) for more than 48hr.
- Recommend LCD module storage conditions is 0 C~40 C <80%RH.
- LCD module should be stored in the room without acid, alkali and harmful gas.
- Avoid dropping & violent shocking during transportation, and no excessive pressure press, moisture and sunlight.
- LCD module can be easily damaged by static electricity. Please maintain an optimum anti-static working environment to protect the LCD module. (eg. ground the soldering irons properly)
- Be sure to ground the body when handling LCD module.
- Only hold LCD module by its sides. Never hold LCD module by applying force on the heat seal or TAB.
- When soldering, control the temperature and duration avoid damaging the backlight guide or diffuser which might degrade the display result such as uneven display.
- Never let LCD module contact with corrosive liquids, which might cause damage to the backlight guide or the electric circuit of LCD module.
- Only clean LCD with a soft dry cloth, Isopropyl Alcohol or Ethyl Alcohol. Other solvents (e.g. water) may damage the LCD.
- Never add force to components of LCD module. It may cause invisible damage or degrade the module's reliability.
- When mounting LCD module, please make sure it is free

## 6. 液晶显示模块设计和使用时须知

- 请注意 V0, VCOM 的设定, 以确保液晶显示模块在不同的使用温度下以及在不同的视角和位置观察模块显示, 均能达到最佳对比度, 请务必将应用电路上设置为对比度可调。
- 请注意液晶显示模块的显示品质判定是指在正常对比度下以及视窗 (V.A) 范围内进行的, 非正常对比度下液晶可能会出现非预期的显示不良, 应注意区分。
- 请勿在最大额定值以外使用液晶显示模块。
- 请勿在没有接通电源的条件下, 给液晶显示模块输送信号。
- 请尽可能缩短信号线的连接, 以避免对液晶显示模块的信号干扰。
- 集成电路因 IC 芯片 (如 TAB 或 COG) 对紫外线极为敏感, 强光环境下可能会引起液晶显示模块功能失效, 故应采用不透光的外壳。
- 请在液晶显示模块与外壳之间保留足够的空间 (可使用衬垫), 以缓冲外力对液晶显示模块的损坏或因受力不均而产生的显示不均匀等异常现象。
- 避免液晶显示屏在某一画面下长时间点亮, 否则有出现残影的风险; 请通过软件每隔一段时间改变一次画面。
- 液晶显示模块的可靠性可能因温度冲击而降低。
- 请勿在阳光直射、高湿、高温或低温下储存和使用液晶显示模块, 这将造成液晶显示模块的损坏或失效。
- 请勿在极限环境 (最大/最小存储/工作温度) 下使用或放置液晶显示模块超过 48 小时以上。
- 液晶显示模块建议存储条件为: 0 C~40 C <80%RH。
- 请勿让液晶显示模块存储于带有酸性, 碱性, 有害气体环境之中。
- 在运输过程中, 请勿让液晶显示模块跌落与猛烈震动, 同时避免异常挤压, 高湿度, 与阳光照射。
- 液晶显示模块极易受静电损坏, 请务必保证液晶显示模块在防静电的工作环境中使用或保存。(如: 烙铁正确接地, 等)
- 拿取液晶显示模块时需注意操作人员的接地情况。
- 请手持液晶显示模块的边沿取放模块, 防止热压纸或 TAB 部位受力。
- 焊接液晶模块时, 请注意控制烙铁的温度、焊接时间, 以免烫坏导光板或偏光片, 导致显示不均匀等不良现象发生。
- 请勿使用洗板水等腐蚀性液体接触液晶模块, 以免腐蚀导光板或模块电路。
- 仅可使用柔软的干布, 异丙醇或乙醇清洁液晶屏表面, 其他任何溶剂 (如: 水) 都有可能损坏液晶模块。
- 请勿挤压液晶显示模块上的元器件, 以避免产生潜在的损坏或失效而影响产品可靠性。

- from twisting, warping and bending.
- Do not add excessive force on surface of LCD, which may cause the display color change abnormally.
  - LCD panel is made with glass. Any mechanical shock (e.g. dropping from high place) will damage the LCD module.
  - Protective film is attached on LCD screen. Be careful when peeling off this protective film, since static electricity may be generated.
  - Polarizer on LCD gets scratched easily. If possible, do not remove LCD protective film until the last step of installation.
  - When peeling off protective film from LCD, static charge may cause abnormal display pattern. The symptom is normal, and it will turn back to normal in a short while.
  - LCD panel has sharp edges, please handle with care.
  - Never attempt to disassemble or rework LCD module.
  - If display panel is damaged and liquid crystal substance leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes promptly wash it off using soap and water.

**7. CTP Mounting Instructions**

**7.1 Bezel Mounting (Figure 1)**

- The bezel window should be bigger than the CTP active area. It should be  $\geq 0.5\text{mm}$  each side.
- Gasket should be installed between the bezel and the CTP surface.  
The final gap should be about  $0.5\sim 1.0\text{mm}$ .
- It is recommended to provide an additional support bracket for backside support when necessary (e.g. slim type TFT module without mounding structure). They should only provide appropriate support and keep the module in place.
- The mounting structure should be strong enough to prevent external uneven force or twist act onto the module.

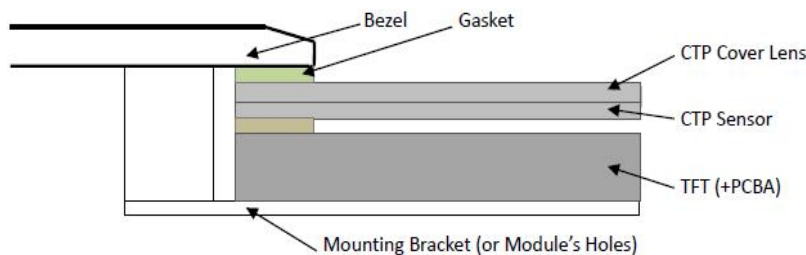


Figure 1

**7.2 Surface Mounting (Figure 2)**

- As the CTP assembling on the countersink area with double side adhesive.  
The countersink area should be flat and clean to ensure the double side adhesive installation result.
- The Bezel is recommend to keep a gap ( $\geq 0.3\text{mm}$  each side) around the cover lens for tolerance.
- It is recommended to provide an additional support bracket with gasket for backside support when necessary (e.g. TFT module without mounding structure). They should only provide appropriate support and keep the module in place.
- The mounting structure should be strong enough to prevent

- 装配液晶显示模块时，请务必注意避免液晶显示模块的扭曲或变形。
- 请勿挤压液晶显示屏表面，这将导致显示颜色的异常。
- 液晶屏由玻璃制作而成，任何机械碰撞(如从高处跌落)均有可能损坏液晶显示模块。
- 液晶屏表面带有保护膜，揭除保护膜时需要注意可能产生的静电。
- 因液晶显示屏表面的偏光片极易划伤，安装完成之前请尽量不要揭下保护膜。
- 请缓慢揭除保护膜，在此过程中液晶显示屏上可能会产生静电，此为正常情况，可在短时间内消失。
- 请注意避免被液晶显示屏的边缘割伤。
- 请不要试图拆卸或改造液晶显示模块。
- 当液晶显示屏出现破裂，内部液晶液体可能流出；相关液体不可吞吃，绝对不可接触嘴巴，如接触到皮肤或衣服，请使用肥皂与清水彻底清洗。

**7. 电容触摸屏安装指导**

**7.1 面框安装（附图1）**

- 客户面框窗口应大于 CTP 动作区域，各边离动作区应  $\geq 0.5\text{mm}$ 。
- 面框与 CTP 面板间应垫有胶垫，其最终间隙约为  $0.5\sim 1.0\text{mm}$ 。
- 建议必要时在背面提供附加支架(例如无安装结构的薄型 TFT 模块)，应仅利用适当支撑以保持模块位置。
- 安装结构应具有足够的强度，以防止外部不均匀力或扭曲力作用到模块上。

**7.2 嵌入安装（附图2）**

- 客户面框应具有使用双面胶粘贴 CTP 的结构沉台面，其粘贴面要求平整且洁净无污以保证粘贴牢靠。
- 考虑到制作误差，建议面框与 CTP 盖板之间四周留有  $\geq 0.3\text{mm}$  间隙。
- 建议必要时在背面提供垫有胶垫附加支架(例如无安装结构的 TFT 模块)，应仅利用适当支撑以保持模块位置。

external uneven force or twist act onto the module

- 安装结构应具有足够的强度，以防止外部不均匀力或扭曲力作用到模块上。

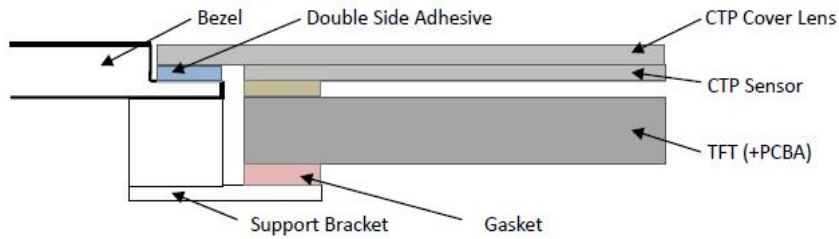


Figure 2

7.3 Additional Cover Lens Mounting (Figure 3)

- For the case of additional cover Lens mounting, it is necessary to recheck with the CTP specification about the material and thickness to ensure the functionality.
- It should keep a 0.2~0.3mm gap between the cover lens and the CTP surface..
- The cover lens window should be bigger than the active area of the CTP.It should be  $\geq 0.5\text{mm}$  each side.
- It is recommended to provide an additional support bracket for backside support when necessary (e.g. slim type TFT module without mounding structure). They should only provide appropriate support and keep the module in place.
- The mounting structure should be strong enough to prevent external uneven force or twist act onto the module.

7.3 覆加盖板 (附图 3)

- 需要覆加玻璃盖板的安装，为确保其功能，有必要查看产品规格书中有关盖板材料和厚度的说明。
- 玻璃盖板与 CTP 表面之间应留有 0.2~0.3mm 间隙。
- 玻璃盖板视窗应大于 CTP 动作区域，各边离动作区应  $\geq 0.5\text{mm}$ 。
- 建议必要时在背面提供附加支架(例如无安装结构的薄型 TFT 模块)，应仅利用适当支撑以保持模块位置。
- 安装结构应具有足够的强度，以防止外部不均匀力或扭曲力作用到模块上。

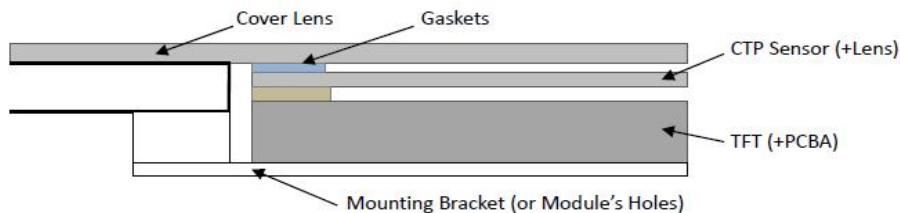


Figure 3

8. RTP Mounting Instructions

- It should bezel touching the RTP Active Area (A.A.) to prevent abnormal touch.It should left gab  $D=0.2\sim 0.3\text{mm}$  in between. (Figure 4)
- Outer bezel design should take care about the area outside the A.A. Those areas contain circuit wires which is having different thickness. Touching those areas could de-form the ITO film. As a result bezel the ITO film be damaged and shorten its lifetime.  
It is suggested to protect those areas with gasket (between the bezel and RTP).The suggested figures are  $B\geq 0.50\text{mm}$ ;  $C\geq 0.50\text{mm}$ . (Figure 4)
- The bezel side wall should keep space  $E= 0.2 \sim 0.3\text{mm}$  from the RTP. (Figure 4)

8. 电阻触摸屏安装指导

- 为避免面框直接压在动作区(A. A.)上造成误操作，面框与电阻触摸屏(RTP)之间应留有一定的空隙  $D=0.2\sim 0.3\text{mm}$  之间。(附图 4)
- 设计面框时，要注意用面框保护触摸屏四周的非保证操作区域，因为布线区域在此处形成一台阶，在此区域附近操作时 ITO Film 变形较大，容易导致 ITO 损坏而降低寿命。为保护 RTP 和避免误操作，在 RTP 与面框之间垫缓冲物 (Gasket)，我们建议设计面框应覆盖动作区的边缘，面框边缘到 V. A. 区的距离  $B\geq 0.50\text{mm}$ ；垫圈内边缘到 V. A. 区的距离  $C\geq 0.50\text{mm}$ 。(附图 4)
- 在设计面框与 RTP 组装时，应考虑到面框内侧与 RTP 外侧的间距  $E\geq 0.2\text{mm}$ 。(附图 4)

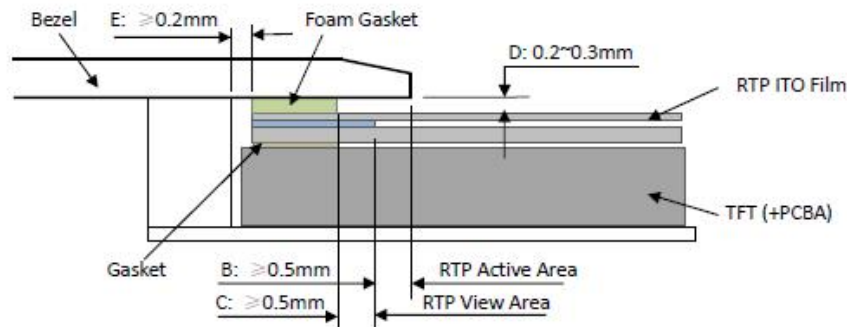


Figure 4

- In general design, RTP V.A. should be bigger than the TFT V.A. and RTP A.A. should be bigger than the TFT A.A. (Figure 5)

- 通常设计时：  
RTP 的可视区 V. A. 应不小于 TFT 的可视区 V. A.  
及 RTP 的动作区 A. A. 应不小于 TFT 的动作区 A. A.  
(附图 5)

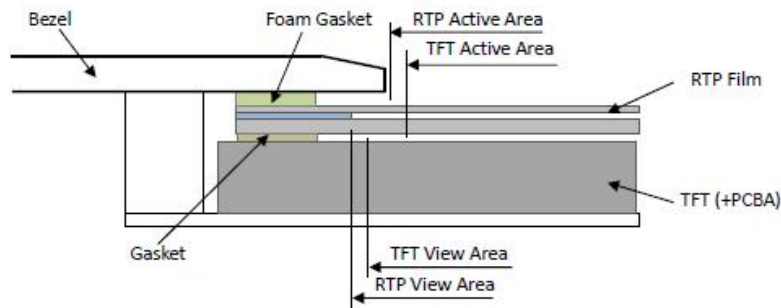
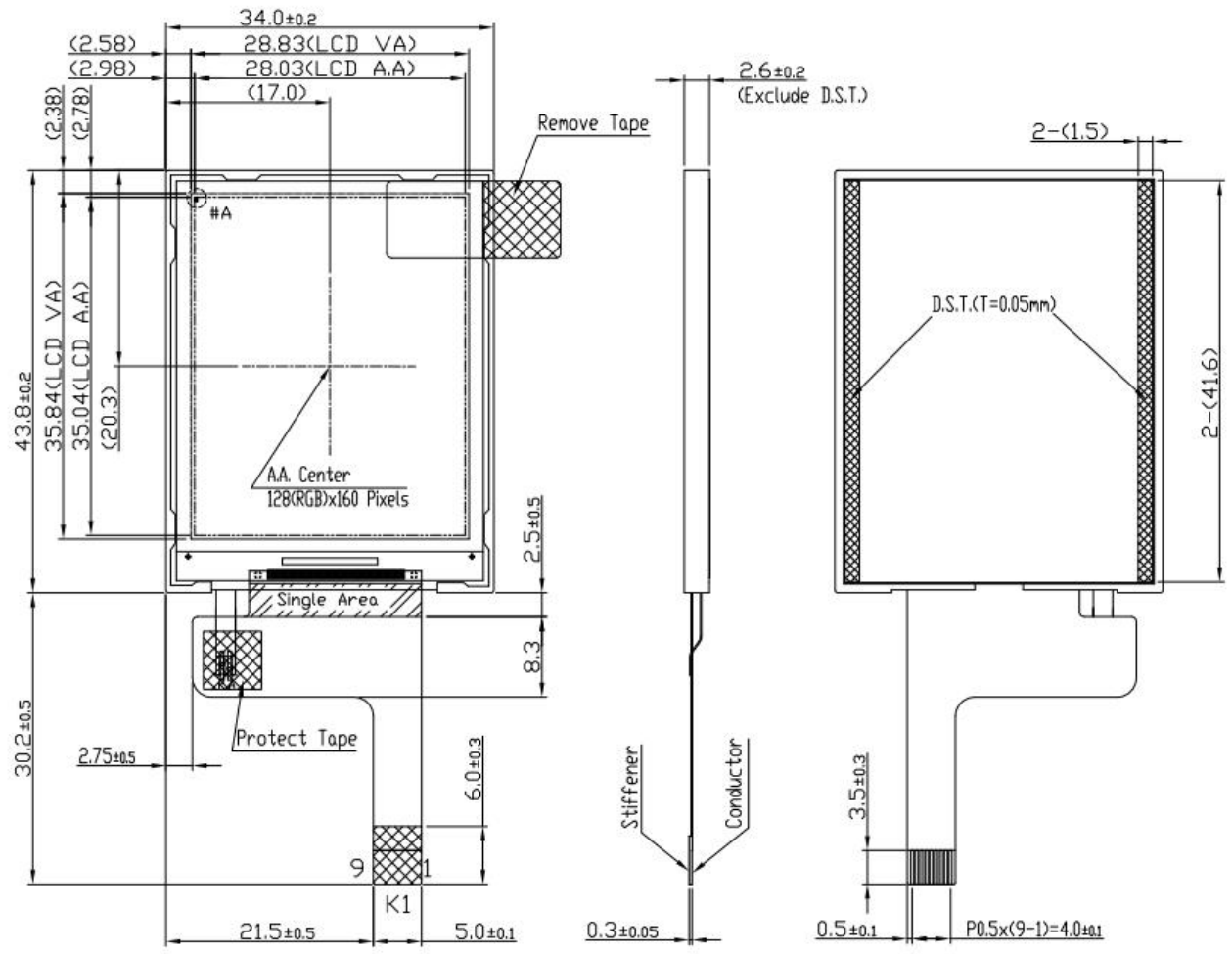


Figure 5

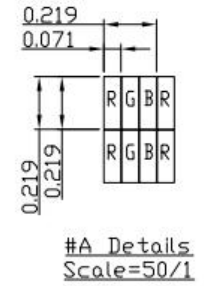
**Warranty**

This product has been manufactured to our company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

- We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.
- We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed our company's acceptance inspection procedures.
- When the product is in CCFL models, CCFL service life and brightness will vary according to the performance of the inverter used, leaks, etc. We cannot accept responsibility for product performance, reliability, or defect, which may arise.
- We cannot accept responsibility for intellectual property of a third part, which may arise through the application of our product to our assembly with exception to those issues relating directly to the structure or method of manufacturing of our product.



K1 Terminal	
No.	Pin Name
1	VSS
2	VDD
3	SCL
4	/RST
5	/CS
6	SDA
7	D/C
8	BLK
9	BLA



- Note:
- \*1. LCD Display Type : TFT,Transmissive
  - \*2. Pixel Arrangement : RGB-STRIPE
  - \*3. Signal Interface : 4-wire SPI
  - \*4. Color Depth : 262k Colors
  - \*5. Operating Voltage : 3.3V
  - \*6. Backlight : White LEDs
  - \*7. Backlight Supply :  $2 \times 15\text{mA}$  (VF=3.2V, TYP)
  - \*8. Recommended Connector : FH19SC-9S-0.5SH(HIROSE) Or Equivalent
  - \*9. Operating Temperature :  $-20^{\circ}\text{C} \sim 70^{\circ}\text{C}$
  - \*10. Storage Temperature :  $-30^{\circ}\text{C} \sim 80^{\circ}\text{C}$

B			
A			
Rev/Note		Date	
Dwg Title	LMT018DNBFD-2 Outline Dwg		
Dwg No.	MK-006923-1-1	Date	2020-06-09
Scale	2/1	Tol.	$\pm 0.3$
Unit	mm	Paper Size	A3
Approved	Checked	Drawn Luo Lin	

**TOPWAY**